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Systems: Design and Implementation, John Wiley, 1999

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He has served as technical program cochair of the 1995 IEEE VLSI Signal Processing workshop and the 1996 ASAP conference, and as the general chair of the 2002 IEEE Workshop on Signal Processing Systems. He was a distinguished lecturer for the IEEE Circuits and Systems society during 1996-1998, and is serving a second term currently (2019-20).

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wish to explore more on improving the efficiency of any digital system, in terms of hardware, speed and low power.

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The objective here is to synthesize molecular reactions for a specified signal processing function. The products of these reactions can be used for protein monitoring and drug delivery. See my personal web page for more details. Publications: K.K. Parhi, VLSI Digital Signal Processing Systems: Design and Implementation, Wiley, NY 1999

Parhi, Keshab | Electrical and Computer Engineering

Professor of Electrical & Computer Engineering, University of Minnesota - Cited by 22,452 - VLSI Signal Processing - VLSI - Machine Learning - Neuroengineering - Molecular Programming ... KJ Cho, KC Lee, JG Chung, KK Parhi. IEEE Transactions on Very Large Scale Integration (VLSI) Systems 12 (5), 522-531 ...

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